SystemC Evolution Fika – 12 September 2023

Introduction

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## SystemC Evolution Fika - Agenda

<table>
<thead>
<tr>
<th>Time (CEST)</th>
<th>Title</th>
<th>Presenter(s)</th>
<th>Affiliation(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>16:00 - 16:10</td>
<td>Welcome &amp; Introduction</td>
<td>Mark Burton(^1), Martin Barnasconi(^2)</td>
<td>(^1) Qualcomm, (^2) NXP</td>
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<tr>
<td>16:10 - 16:40</td>
<td>SystemC IEEE 1666 update</td>
<td>Jérôme Cornet</td>
<td>STMicroelectronics, IEEE P1666 Chair</td>
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<tr>
<td>16:40 - 17:10</td>
<td>Tracing in SystemC</td>
<td>Lukas Jünger(^1), Eyck Jentzsch(^2), Rocco Jonack(^2)</td>
<td>(^1) MachineWare GmbH, (^2) MINRES Technologies</td>
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<tr>
<td>17:10 - 17:30</td>
<td>Q&amp;A</td>
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\(^1\) Qualcomm, \(^2\) NXP
Federated Simulation Standard (FSS)
Enabling cross-industry interoperability of simulation frameworks

• Objective: Cross-industry collaboration to improve the interoperability of product and environment simulation
• Approach: Leveraging and connecting existing standards and industry formats
• Introduce standardized interfaces
• Targeting a scalable simulation and modeling ecosystem, covering different levels of the ‘OSI stack’
FSS Proposed Working Group & First meeting

• **Call for Participation** is now open!
  – PWG is open for everyone – no Accellera membership required
  – More information and PWG registration: [https://www.accellera.org/activities/proposed-working-groups](https://www.accellera.org/activities/proposed-working-groups)

• First in-person meeting **September 25+26, Toulouse, France**
  – Day 1: Discuss industry use cases, requirements, applications
  – Day 2: Organizational aspects incl. alignment with other initiatives or organizations
  – More information and event registration: [https://evenium.events/8jxrclxh](https://evenium.events/8jxrclxh)

• Feel free to contact the organization committee for questions and additional information
  – Martin Barnasconi, Mark Burton, Eric Jenn
Next up – SystemC Evolution Day 2023

• Mark your calendar
  – Date: **16 November 2023** (day after DVCon Europe 2023)
    Time: 09:30 - 17:30 CET
    Location: Holiday Inn Munich City Centre, Hochstrasse 3, 81669 Munich, Germany

• **Call for Presentations** still open
  – Submit your ideas and contributions for additions and improvements to SystemC and/or its extensions covering TLM, CCI, AMS, Synthesis and/or Verification
  – A contribution can be in the form of a finding, a result, or a proposal, but also a problem experienced that you feel needs attention and discussion in an Accellera and SystemC user-community context

• More information on the event and abstract submission process:
  [https://systemc.org/events/sced2023/](https://systemc.org/events/sced2023/)
Welcome to the SystemC Community Portal

Your online reference for everything related to SystemC, the language for system-level design, high-level synthesis, modeling and verification.