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Towards a Unified Solution for

SystemC Tracing and Analysis

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Speaker Introduction

- Co-Founder and Managing Director at MachineWare GmbH
- Working on simulation topics since 2017 (GreenSocs, Synopsys, ...)
- Scientific background
 - PhD researcher at RWTH Aachen until 2023
 - Topics: Full System Simulation, Novel Acceleration Techniques
- Active at Accellera Systems Initiative
 - Chair of SystemC CCI Working Group
 - Member of SystemC Language Working Group and Common Practices Subgroup







Virtual Platforms

- Virtual Platform: Full System Simulation
- Use cases:
 - SW verification & analysis
 - Architecture exploration
- Generates simulation data
 - SystemC data (events, processes, TLM, ...)
 - Performance numbers
 - Target SW behavior
 - CPU cache usage
 - ο.
- User cares most about resulting data





Problem Statement

- No standard for storing simulation data
 - Except VCD for signals
- No standardized format for data
 - Low hanging fruit: SystemC data
 - More complex: Model-specific data
- Even worse in federated simulation
- Need to evolve SystemC to cover this





Proposed Solution

- Step 1: Align on storage format
 - Our proposal: SQLite database
- Divide and conquer
 - SystemC data first, more complex data later
- Evaluated at ICE, RWTH Aachen: SIMDAL¹



¹ <u>https://github.com/nbosb/simdal</u>



Proposed Solution

- Step 1: Align on storage format
 - Our proposal: SQLite database
- Divide and conquer
 - SystemC data first, more complex data later
- Evaluated at ICE, RWTH Aachen: SIMDAL¹
- Development at MachineWare: InSCight
- SystemC TLM-2.0 compatible

¹ <u>https://github.com/nbosb/simdal</u>





Performance Analysis

• But isn't SQLite too slow? No.



AVP64: https://github.com/aut0/avp64



Summary

- Take away: SQLite works fine
- Important: Align on trace format
- Want to learn more?
 - Follow us on LinkedIn <u>mwa.re/li</u>
 - Star us on GitHub <u>mwa.re/gh</u>
 - E-mail me at <u>lukas@mwa.re</u>
 - Join Accellera and get involved



