

SystemC Evolution Day 2024

Mark Burton

Agenda

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| 9:30 | Welcome & Introduction | Mark Burton |
| 9:40 | Keynote: QEMU And Simulation environments outside SystemC | Alex Bennée, Linaro |
| 10:20 | Coffee Break | |
| 10:40 | Panel: Thinking big on SystemC 4.0 | |
| 12:00 | Lunch | |
| 13:30 | Working Group and Community Update | Martin Barnasconi |
| 14:00 | CCI Update and Demo | Peter De Jager Lukas Jünger |
| 14:30 | SystemC TLM-2.0 Loosely-Timed Contention-Aware Modeling | Emad Arasteh, Chapman University, Orange, CA |
| 15:00 | Coffee break | |
| 15:20 | Open room discussion: SystemC Multi-kernel support and thread safety | Peter de Jager, Mark Burton |
| 16:45 | Closure | |

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Panel: Thinking big on SystemC 4.0

Thinking about SystemC 4.0 ... what should we do?

How can SystemC be used by the wider simulation community?

How do other communities handle similar problems to SystemC?

How does the SystemC development and standardization cycle look like?

How do we get there...

Panelists

Mark Burton, Qualcomm

Volkan Esen, Infineon

Christoph Grimm, University of Kaiserslautern-Landau

Rocco Jonack, Arteris

Lukas Jünger, MachineWare

Holger Riethmueller, Robert Bosch GmbH

QEMU and SystemC....

QEMU is open source, used by millions, has a massive user and developer community – what can we learn?

What components make up QEMU?

How does 'startup' work?

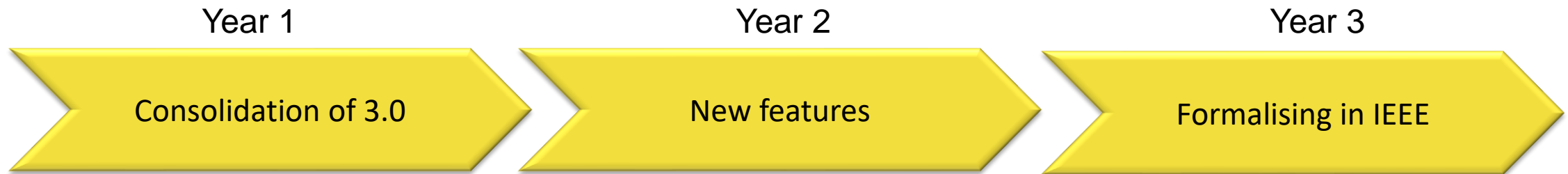
How does 'Configuration' 'inspection' etc work?

How does TIME work in QEMU – events etc

Should we explore a stronger collaboration between the QEMU and SystemC communities (and even explore a 'merger')?

SystemC 4.0 - Timeline

- 3 year cycle – we're already ½ way through !
- Can we really stick with this cadence? Only with YOUR HELP!



- So – what could be in SystemC 4.0
- And what is beyond?