

# SystemC Fika - April 2026

Mark Burton  
SystemC Fika Chair

[systemc.org](http://systemc.org)

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# Agenda

Time (CEST)	Title	Presenter(s)
16:00 - 16:05	Welcome & Introduction	Mark Burton, SystemC Fika Chair
16:05 - 16:15	Brief SystemC standardization update	Martin Barnasconi, Accellera Technical Committee Chair
16:15 - 16:45	CHERI-RISC-V VP++: Exploring CHERI using a SystemC-based Virtual Platform	Andreas Hinterdorfer, Manfred Schlägl, Daniel Große, JKU Institute for Complex Systems
16:45 - 17:15	TvastaaVP: An agentic AI approach to SystemC	Karthick Gururaj, Vayavyalabs
17:15 - 17:45	Toward Fast Heterogeneous Virtual Prototypes: Increasing the Solver Efficiency in SystemC AMS	Alexandra Kuester, Bosch Sensortec GmbH
17:45 - 17:55	Brief report-out from the SystemC CCI "Sprint"	Lukas Jünger, SystemC CCI WG chair
17:55 - 18:00	Closing	Mark Burton, SystemC Fika Chair

# Brief SystemC standardization update

Fika April 2026

Martin Barnasconi  
Accellera Technical Committee Chair

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# Outline

- Accellera Systems Initiative
- SystemC working groups update
- [systemc.org](http://systemc.org)
- How to join us

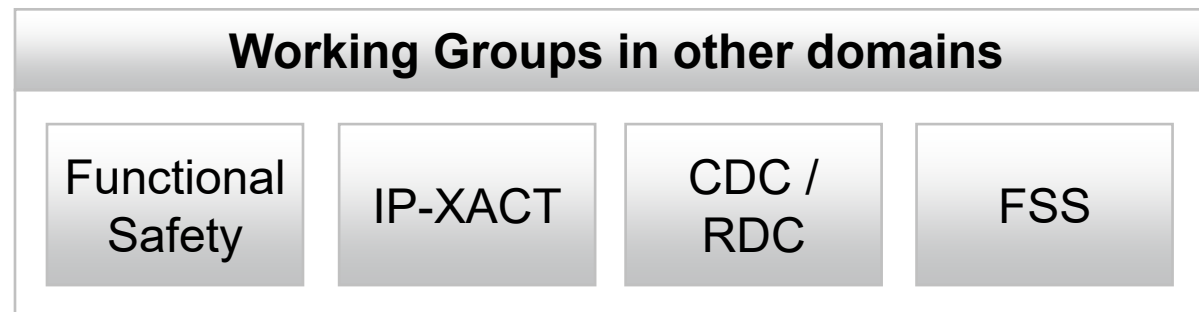
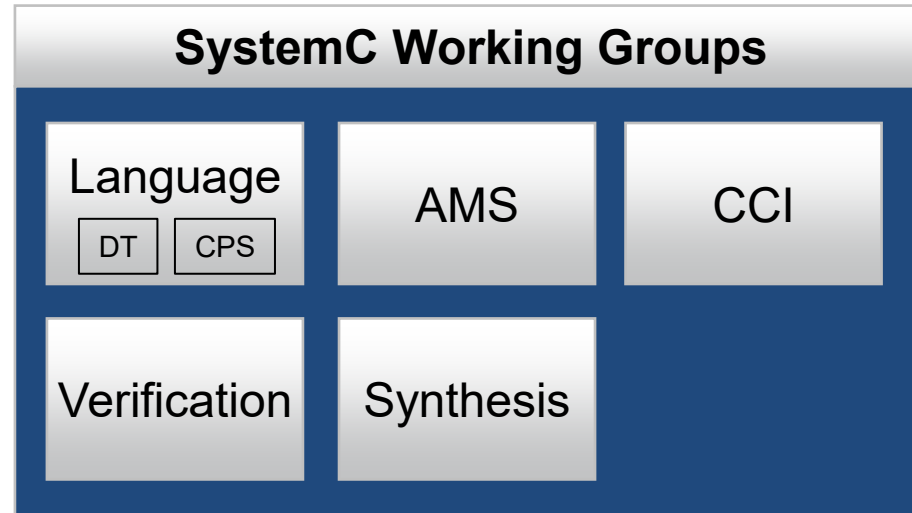
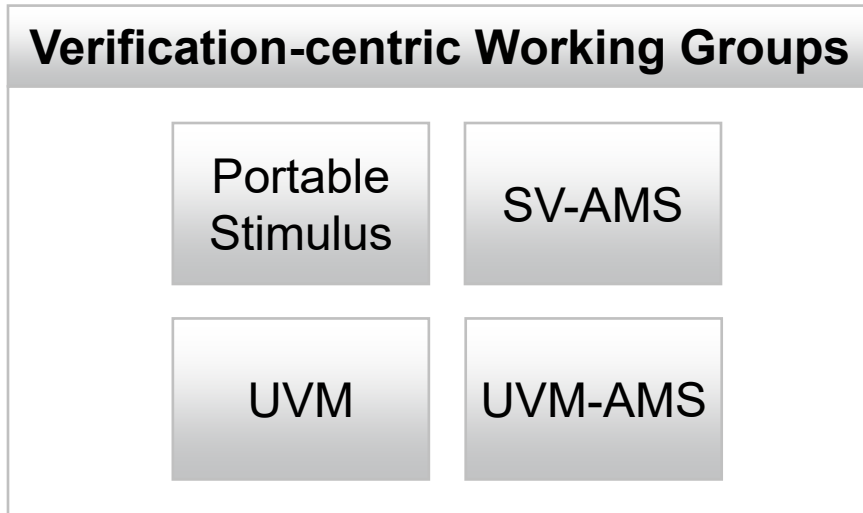
# Accellera Systems Initiative

## Our Mission

To provide a platform in which the electronics industry can collaborate to innovate and deliver global standards that improve design and verification productivity for electronics products.



# Accellera Working Groups



# SystemC Working Groups

- SystemC Language Working Group (LWG)
  - Chair: Laurent Maillet-Contoz (ST)
  - Subgroups:
    - Common Practices (CPS): Chair: Mark Burton (Qualcomm)
    - Data Types (DT): Chair: Mike Meredith (Cadence)
- SystemC Analog/Mixed-Signal Working Group (AMSWG)
  - Chair: Martin Barnasconi (NXP)
- SystemC Configuration, Control & Inspection Working Group (CCIWG)
  - Chair: Lukas Jünger (MachineWare)
- SystemC Synthesis Working Group (SWG)
  - Chair: Frederic Doucet (Qualcomm)
- SystemC Verification Working Group (VWG)
  - Chair: Thilo Vörtler (COSEDA Technologies)

# SystemC Working Groups update (1)

- SystemC Language Working Group
  - Preparing next revision of IEEE1666 language standard (current standard released in 2023, Corrigendum in 2025)
  - Ongoing review, approval and implementation into reference implementation (“SystemC 4”)
  - Common Practices team finalizing new logging API
- SystemC Analog/Mixed-Signal Working Group
  - Preparing next revision of IEEE1666.1 language standard (current standard released in 2016)
  - Ongoing work to make SystemC AMS regression suite available as public repository
- SystemC Configuration, Control & Inspection Working Group (CCIWG)
  - CCI inspection API available in experimental namespace
  - First proposals for control/command API
  - Initiated first coding-focused event “SystemC Sprint”

# SystemC Working Groups update (2)

- SystemC Synthesis Working Group (SWG)
  - Ongoing work to update the SystemC Synthesizable Subset standard
  - Developing supplemental material to support High Level Synthesis concepts (“Minilib”)
- SystemC Verification Working Group (VWG)
  - Extensive review and update of the UVM-SystemC Language Reference Manual as preparation of the 1.0 standard release
  - In parallel, updating the UVM-SystemC reference implementation, focus on LRM compliancy, performance and robustness

# systemc.org

- The SystemC community portal, containing
  - SystemC Evolution Day Events and Fikas: announcements, presentations and videos
  - Added more **Open Access** Publications
  - Adding **libraries and projects**
- **YOU** can help in adding content!
  - Submit your pull request to [github.com/accellera-official/systemc.org](https://github.com/accellera-official/systemc.org)

SYSTEMC™ Home Overview ▾ Events ▾ Resources ▾ Playground Get Involved 🔍 Search GitHub

## Welcome to the SystemC Community Portal

Your online reference for everything related to SystemC, the language for system-level design, high-level synthesis, modeling and verification.

[Overview](#) [Downloads](#)

```
1 #include <systemc>
2 #include "fifo.h"
3 #include "producer.h"
4 #include "consumer.h"
5
6 class top : public sc_core::sc_module
7 {
8     public:
9         fifo fifo_inst("Fifo1");
10        producer prod_inst("Producer1");
11        consumer cons_inst("Consumer1");
12
13        top(sc_core::sc_module_name name)
14        : sc_core::sc_module(name)
15        {
16            prod_inst.out(fifo_inst);
17            cons_inst.in(fifo_inst);
18        }
19    };
20
21 int sc_main (int, char *[]) {
22     top top1("Top1");
23     sc_core::sc_start();
24     return 0;
25 }
```

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# How to join us

- Become an Accellera Working Group member
  - [Join Accellera](#) and participate in the Accellera working groups
  - Direct access to the latest standardization proposals and development implementations
- Become a member of the IEEE Standards Association
  - Join [IEEE-SA](#) to participate in the IEEE P1666 (SystemC) or IEEE P1666.1 (SystemC AMS) working groups
- Share your experiences
  - Report your issues or create pull requests on the Accellera public repositories on [GitHub](#)
  - Join the community forums at [forums.accellera.org](http://forums.accellera.org)
- Help us to grow the SystemC ecosystem and community
  - Contribute to the SystemC community portal [systemc.org](http://systemc.org)
  - Participate in community events such as the [SystemC Evolution Day, Fika and Sprint](#)

Thank You

Q&A

